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INTELLECTUAL PROPERTY LAW

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December 14, 2004

Receiver: Anh D. Mai, Patent Examiner, Art Unit 2814

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Sender: Francis T. Kalinski II, Esq.

Our Ref. No.: 02-6037/LSI1P218

Re: U.S. Patent Application No. 10/698,169

File Date: October 31, 2003

Title: "Memory Device Having an Electron Trapping Layer in a High-K Dielectric Gate Stack"

Pages Including Cover Sheet(s): 2

MESSAGE:

CONFIDENTIALITY NOTE

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re application of:** ARONOWITZ et al.**Attorney Docket No.:**
02-6037/ LSI1P218**Application No.:** 10/698,169**Examiner:** MAI, Anh D.**Filed:** October 31, 2003**Group:** 2814**TITLE:** MEMORY DEVICE HAVING AN
ELECTRON TRAPPING LAYER IN A HIGH-K
DIELECTRIC GATE STACK**RECEIVED**
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CERTIFICATE OF FACSIMILE TRANSMISSION
 I hereby certify that this correspondence is being transmitted by facsimile to fax
 number 703-872-9306 to the U.S. Patent and Trademark Office on December 14,
 2004.

Signed: Sue Funchess
 Sue Funchess

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement dated November 19, 2004, Applicants hereby elect, without traverse, group I, drawn to device claims 1-11 to prosecute in the above-identified patent application. Moreover, the Applicants elect to prosecute Species B, which includes **Claims 1, 2, 4, 5, 6, 10, and 11.**

If any fees are due in connection with the filing this Response to Restriction Requirement, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 12-2252 (Order No. 02-6037).

Respectfully submitted,

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